

Attorney Docket: 082124-0306006
Client Reference: 2001-OPH-2313/JYP-div



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re PATENT APPLICATION of: CHO et al.
Application No.: 10/663,910

Confirmation Number: 2642
Group Art Unit: 2818

Filed: September 17, 2003

Examiner: Ho

Title: CMOS of Semiconductor Device and Method for Manufacturing the Same


COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Sir:

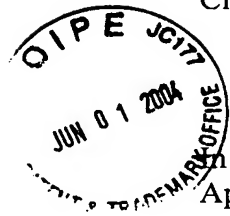
In reply to the Examiner's Statement of Reasons for Allowance dated March 4, 2004, it is respectfully submitted that claims 1-6 are patentable as the prior art neither discloses nor suggests a complementary metal oxide semiconductor (CMOS) device comprising a first gate electrode having a stacked structure of a first metal layer, a polysilicon layer and a second metal layer formed in a p-well region of a cell region and an n-well region of a peripheral circuit region and a second gate electrode having a stacked structure of the polysilicon layer and the second metal layer formed on a p-well of a peripheral circuit region.

Respectfully submitted,
PILLSBURY WINTHROP LLP


John P. Darling
Reg. No. 44,482
Tel. No. (703) 905-2045
Fax No. (703) 905-2500

Date: June 1, 2004
P.O. Box 10500
McLean, VA 22102
(703) 905-2000

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REQUEST FOR RETURN OF ORIGINAL RECORDED ASSIGNMENT

Assignment Branch
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

On September 27, 2003 an Assignment was filed in the above-identified application. As of the present date, however, the original recorded assignment has not been returned to the offices of the undersigned.

Enclosed are copies of the recordation cover sheet, the Assignment and our PTO receipt dated September 17, 2003 for your convenience.

The undersigned respectfully requests the return of the recorded Assignment.

Respectfully submitted,
PILLSBURY WINTHROP LLP

John P. Darling
Reg. No. 44,482
Tel. No. (703) 905-2045
Fax No. (703) 905-2500

Date: June 1, 2004
P.O. Box 10500
McLean, VA 22102
(703) 905-2000

Enclosures:

Copy of PTO Receipt dated Sept. 17, 2003
Copy of Recordation Cover Sheet dated Sept. 17, 2003
Copy of Assignment

PAT-103 5/02 PTO RECEIPT FOR INDICATED ITEMS

Appln. No: 0 /New

Inventor(s) CHO et al.

Title: CMOS of Semiconductor Device and Method

Atty GJP/dlh

Date 9/17/03

C# 82124

M# 306006

ENCLOSED:

☒ Amendment ☐ Appendix ☒ Cover sheet

#1 No. of Pages Abstract

#16 No. of Pages Spec and Claims

#17 No. of numbered Claims only

☒ Declaration (3 #pgs)

☒ Assignment ☒ Cover Sheet

No. of Priority Documents

#18 No. Sheets Drawings (Fig(s) 1 to 4G) ☒ PTO-1449

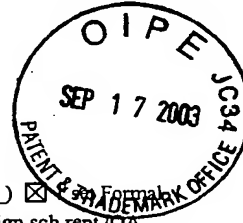
☐ IDS Letter ☐ cited Appl(s) ☐ Foreign sch rept

☒ PTO-1449 ☐ cited docs.

\$ 790 To be charged to Dep. Acct. No. 03-3975

Other: Application Data Sheet, Fee Transmittal

CURRENT DUE DATE: September 17, 2003



RECORDATION FORM COVER SHEET
 PATENT APPLICATIONS & PATENTS ONLY

TO THE DIRECTOR OF THE US PATENT AND TRADEMARK OFFICE:

SIR: PLEASE RECORD THE ATTACHED ORIGINAL DOCUMENTS OR COPY THEREOF.

1. NAME OF CONVEYING PARTY(IES) (ASSIGNORS(S)):

1. Heung Jae CHO

3. Kwan Yong LIM

5.

7.

2. Dae Gyu PARK

4.

6.

8.

ADDITIONAL NAME(S) OF CONVEYING PARTY(IES) ATTACHED? ☐ YES ☒ NO

2. PARTY(IES) (ASSIGNEE(S)) RECEIVING INTEREST:

NAME: Hynix Semiconductor Inc.

ADDRESS: San 136-1, Ami-ri, Bubal-eub, Ichon-shi, Kyoungki-do, 467-860, Korea

ADDITIONAL NAME(S) & ADDRESS(ES) ATTACHED? ☐ YES ☒ NO

3. NATURE OF CONVEYANCE (DOCUMENT):

(Submit herewith only one document for recordation—multiple copies of same Assignment signed by different inventors is one document)☒ ASSIGNMENT OF☒ WHOLE☐ PART INTEREST☐ ORIGINAL☒ FACSIMILE/PHOTOCOPY☐ CHANGE OF NAME☐ VERIFIED TRANSLATION☐ SECURITY ☐ MERGER☐ OTHER:

EXEC. DATE: September 10, 2002

EXECUTION DATE(S) ON THE DECLARATION IF FILED HERewith: (NOTE: IF DATES ON DECLARATION AND ASSIGNMENT DIFFER SEE ATTY!) September 10, 2002

4.5 APPL. NO.(S) OR PAT NO.(S). OTHERS ON ADDITIONAL SHEET(S) attached? ☐ YES ☒ NO

A. PAT. APP. NO.(S) series code/serial no.	M#	1 st INVENTOR if not in item 1	B. PATENT NO(S)	M#	1 st INVENTOR if not in item 1
New	306006				

5. Name & Address of Party to Whom Correspondence
Concerning Document Should be Mailed:Pillsbury Winthrop LLP
Intellectual Property Group
P.O. Box 10500 McLean, VA 22102

6. NUMBER INVOLVED:

APPLNS 1 + PATS 0 = TOTAL = 1

7. AMOUNT OF FEE DUE: (Code 581)

ABOVE TOTAL x \$40 = \$40

5.5 ATTY DKT:

P 306006

2001-OPH-2313-Div

8. PLEASE CHARGE TO OUR DEPOSIT ACCOUNT
NUMBER: 03-3975

UNDER ORDER NO

82124

306006

MATTER NO.

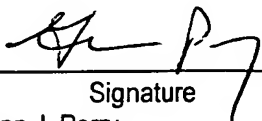
CLIENT REF.

dup. sheet not required

CLIENT NO.

MATTER NO.

9. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.



Signature

Attorney: Glenn J. Perry

Reg. No. 28458

Atty/Sec: gjp/dlh

TEL: (703) 905-2161

Date: September 17, 2003

FAX: (703) 905-2500

10. Total number of pages including this
cover sheet, attachments and document
(do not file dup. Cover sheet)

2

FILE WITH PTO RETURN RECEIPT (PAT-103A)

Please return signed/recorded to:

Pillsbury Winthrop LLP
Intellectual Property Group
1600 Tysons Boulevard
McLean, Virginia 22101

USA Patent Appln.
Sole or Joint

of inventions made outside USA
executed with or after application

Atty. Dkt. P

M#

Client Ref.

NONPROVISIONAL

ASSIGNMENT
OF NONPROVISIONAL APPLICATION

NONPROVISIONAL

In consideration of the sum of Ten Dollars (\$10.00) and other good and valuable consideration paid to each of the undersigned, to wit:

INSERT
NAME(S) OF
INVENTOR(S)

(1) Heung Jae CHO	(2) Dae Gyu PARK
(3) Kwan Yong LIM	(4)
(5)	<input type="checkbox"/> box if continued on page 2

the receipt and sufficiency of which are hereby acknowledged by the undersigned who at the request of, hereby sell(s), assign(s) and transfer(s) unto:

INSERT
NAME(S) OF
ASSIGNEE(S)
& ADDRESS(ES)

Hynix Semiconductor Inc.
San 136-1, Ami-ri, Bubal-eub, Ichon-shi, Kyoungki-do 467-860, Korea

(hereinafter designated "ASSIGNEE") the entire right, title and interest for the United States of America as defined in 35 U.S.C. 100, in the invention and all applications including any and all divisions, continuations, substitutes, and reissues thereof; and all resulting patents, known as

TITLE OF
INVENTION

CMOS OF SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

for which the undersigned executed an application for Letters Patents of the United States of America:

NOTE → →

(Complete

line A, B and/or C)

(A) ☒ even date herewith

(B) ☐ on

(C) ☐ in U.S. Appln. No. / filed

AND the undersigned hereby authorize(s) and request(s) the United States Commissioner of Patents and Trademarks to issue said Letters Patent to the said ASSIGNEE, for its interest as ASSIGNEE, its successors, assigns and legal representatives; the undersigned agree(s) that the attorney of record in said application shall hereinafter act on behalf of said ASSIGNEE;

AND the undersigned hereby agree(s) to testify and execute any papers for ASSIGNEE, its successors, assigns and legal representatives, deemed essential by ASSIGNEE to ASSIGNEE'S full protection and title in and to the invention hereby transferred.

NOTE → → The undersigned hereby authorize(s) Pillsbury Winthrop LLP of the above address to insert hereon any further identification necessary or desirable for recordation of this document.

INVENTORS

1):
Name: Heung Jae CHO

DATE SIGNED

Sep. 10, 2002

WITNESSES

Soyoung KIM

2):
Name: Dae Gyu PARK

Sep. 10, 2002

Soyoung KIM

3):
Name: Kwan Yong LIM

Sep. 10, 2002

Soyoung KIM

Name:

Name: